Commodity Single Board Computer Clusters and their Applications

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Abstract

Current commodity Single Board Computers (SBCs) are sufficiently powerful to run mainstream operating systems and workloads. Many of these boards may be linked together, to create small, low-cost clusters that replicate some features of large data center clusters. The Raspberry Pi Foundation produces a series of SBCs with a price/performance ratio that makes SBC clusters viable, perhaps even expendable. These clusters are an enabler for Edge/Fog Compute, where processing is pushed out towards data sources, reducing bandwidth requirements and decentralising the architecture. In this paper we investigate use cases driving the growth of SBC clusters, we examine the trends in future hardware developments, and discuss the potential of SBC clusters as a disruptive technology. Compared to traditional clusters, SBC clusters have a reduced footprint, are low-cost, and have low power requirements. This enables different models of deployment – particularly outside traditional data center environments. We discuss the applicability of existing software and management infrastructure to support exotic deployment scenarios and anticipate the next generation of SBC.

We conclude that the SBC cluster is a new and distinct computational deployment paradigm, which is applicable to a wider range of scenarios than current clusters. It facilitates Internet of Things and Smart City systems and is potentially a game changer in pushing application logic out towards the network edge.

Keywords: Raspberry Pi, Edge Computing, Networks Cloud computing, Centralization / decentralization, Distributed computing methodologies, Multicore architectures, Emerging architectures

1. Introduction

Commodity Single Board Computers (SBCs) are now sufficiently powerful that they can run standard operating systems and mainstream workloads. Many such boards may be linked together, to create small low-cost clusters that replicate features of large data centers, and that can enable new fog and edge compute applications where computation is pushed out from the core of the network towards the data sources. This can reduce bandwidth requirements and latency, help improve privacy, and decentralise the architecture, but it comes at the cost of additional management complexity. In this paper we investigate use cases driving the growth of SBC clusters, examine the trends in future hardware developments and cluster management, and discuss the potential of SBC clusters as a disruptive technology.

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The introduction of the Raspberry Pi has led to a significant change in the Single Board Computer (SBC) market. Similar products such as the Gumstix have been available since 2003 \cite{1}, however, the Raspberry Pi has sold in much higher volumes leading to the company behind it being the fastest growing computer company in the world \cite{2}. This has led to a dramatic increase in the number of SBC manufacturers and available products, as described in Section 2. Each of these products has been subject to different design decisions leading to a large variation in the functions available on the SBC. The low price point of SBCs has enabled clusters to be created at a significantly lower cost than was previously possible. We review prototypical SBC clusters in Section 3.

The purchasing of a multi-node cluster has been made significantly cheaper by the developments of SBCs but the challenges of setup and ongoing maintenance remain. Some of these challenges are also experienced when running a standard cluster, however, issues such as SD card duplication and low-voltage DC distribution are unique to the creation of SBC clusters. Our contribution is to identify these differences and show where existing cluster management techniques can be used, and where new techniques are needed. Management tasks are further com-
plicated by the fact that traditional clusters are located within data centers due their high power demands, it is fea-
sible for an SBC cluster to be geographically distributed, fur-
ther complicating management tasks. Section 5 discus-
ses the major challenges.

These clusters can be created simply to gain an un-
derstanding of the challenges posed by such develop-
ments and can also have practical uses where a traditional
cluster would not be appropriate. The first clusters, e.g.
IridisPi [3] and The Glasgow Pi Cloud [4], were created
primarily for education. Since then clusters have been
created to manage art work [5, 6] or provide disposable
compute power to extreme environments, in which node
destruction is likely [7]. Section 4 highlights classes of use
cases for this technology, including for emergent Fog and
Edge Compute applications. The creation of SBC clus-
ters is not a very mature area of research. In this paper,
we survey current achievements and outline possible topi-
cs for future work. Finally, we discuss future applications for
such systems in Section 6.

2. Single board computer overview

“A Single Board Computer (SBC) is a complete com-
puter built on a single circuit board, with micropro-
cessor(s), memory, input / Output (I/O) and other features
required of a functional computer.” [8]. This definition
does not fully capture what it means to be an SBC so we
compared SBC and other platforms to identify key dif-
fences with the results summarised in Table 1. Although
the definition given above incorporates most of the factors
it ignores three major differences; the availability of built
in general purpose I/O ports, power draw and cost. It is
the inclusion of such ports and a low price that means
SBCs fall into the gap between controller boards and PCs.
The similarity between SBCs and smartphones is interest-
ing to note, and the similarities continue as the majority
of SBCs are ARM processors as opposed to the Intel/AMD chips currently used in the PC market.

When the Raspberry Pi was released there were other
SBCs such as the Gumstix [1] and the BeagleBone which
had very similar technical specifications (although were
more expensive) [9]. Both the Raspberry Pi and the Bea-
gleBone have enabled them to be deployed into situations in which a standard PC would not be suitable, but the process-
ing requirements cannot be met by micro-controllers. Ex-
amples of such uses include collection of high resolution
(12MP) images of rock-faces in the Swiss Alps [13], and
controlling sensor networks on Icelandic glaciers [14].

When an SBC has a removable storage medium,
any software fault can be recovered from by wiping the
SD card and starting again, a trivial process compared to
reinstalling a PC from scratch.

The low-cost and power consumptions of the SBCs
have enabled them to be deployed into situations in which
a standard PC would not be suitable, but the process-
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Wireless sensor networks have benefited from the low
cost and power consumptions of the SBCs. The creation of SBC clusters can be created simply to gain an understand-
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Edge Compute applications. The creation of SBC clusters is not a very mature area of research. In this paper,
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cs for future work. Finally, we discuss future applications for
such systems in Section 6.

One of the main advantages of the SBC is the low-cost,
which has been described as costing “a few weeks’ pocket
money” [2]. This enables children to buy it for themselves and relaxes parents about replacement costs, in the event
of damage. Projects which require multiple SBCs are also
within reach and in some cases the SBC has become a
standard building block for projects; driven by the abun-
dance of examples, documentation and supporting soft-
ware. When an SBC has a removable storage medium,
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Wireless sensor networks have benefited from the low
cost and power consumptions of the SBCs. This also brings advantages in traditional data centers. It has been shown that by using a cluster of multiple Raspberry Pi (B) as opposed to mul-
tiple ‘standard’ servers, a reduction in power consumption
between 17x and 23x can be observed [15]. The figure is
Table 1: Comparison between Personal Computer (PC), Controller Board (CB), Smartphone, and Single Board Computer (SBC) (DB = daughter-board, N/A = not-available, ROM = read-only memory, RW-Ext = read-write external storage)

<table>
<thead>
<tr>
<th>Component</th>
<th>PC</th>
<th>CB</th>
<th>Smartphone</th>
<th>SBC</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU</td>
<td>DB</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>GPU</td>
<td>Yes, DB</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>Memory</td>
<td>DB</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>LAN</td>
<td>Yes, DB</td>
<td>N/A</td>
<td>N/A</td>
<td>Yes</td>
</tr>
<tr>
<td>Display Port</td>
<td>Yes, DB</td>
<td>N/A</td>
<td>N/A</td>
<td>Yes</td>
</tr>
<tr>
<td>Storage</td>
<td>ROM, RW-Ext</td>
<td>ROM</td>
<td>ROM, RW-Ext</td>
<td>ROM, RW-Ext</td>
</tr>
<tr>
<td>GPIO Header</td>
<td>Yes, (USB)</td>
<td>Yes</td>
<td>N/A</td>
<td>Yes</td>
</tr>
</tbody>
</table>

Table 2: Example System on a Chip (SoC) hardware used in Single Board Computers

<table>
<thead>
<tr>
<th>SoC</th>
<th>Cores / Clock</th>
<th>Architecture</th>
<th>GPU</th>
</tr>
</thead>
<tbody>
<tr>
<td>Allwinner H3</td>
<td>4 x 1.6 GHz</td>
<td>32 bit</td>
<td>Mali-400 MP2</td>
</tr>
<tr>
<td>Allwinner R18</td>
<td>4 x 1.3 GHz</td>
<td>64 bit</td>
<td>Mali-400 MP2</td>
</tr>
<tr>
<td>Altera Max 10</td>
<td>2k logic cell FPGA</td>
<td>32 bit</td>
<td>-</td>
</tr>
<tr>
<td>Amlogic S905</td>
<td>4 x 1.5 GHz</td>
<td>32 bit</td>
<td>Mali-450</td>
</tr>
<tr>
<td>Broadcom BCM2835</td>
<td>1 x 700 MHz</td>
<td>32 bit</td>
<td>Broadcom VideoCore IV</td>
</tr>
<tr>
<td>Broadcom BCM2836</td>
<td>4 x 900 MHz</td>
<td>32 bit</td>
<td>Broadcom VideoCore IV</td>
</tr>
<tr>
<td>Broadcom BCM2837</td>
<td>4 x 1.2 GHz</td>
<td>64 bit</td>
<td>Broadcom VideoCore IV</td>
</tr>
<tr>
<td>Broadcom BCM2837B0</td>
<td>4 x 1.4 GHz</td>
<td>64 bit</td>
<td>Broadcom VideoCore IV</td>
</tr>
<tr>
<td>Exynos 5422</td>
<td>4 x 2.1 GHz &amp; 4 x 1.5 GHz</td>
<td>32 bit</td>
<td>Mali-T628 MP6</td>
</tr>
<tr>
<td>Intel Pentium N4200</td>
<td>4 x 1.1 GHz</td>
<td>32 bit</td>
<td>Intel HD Graphics 505</td>
</tr>
<tr>
<td>Sitara AM3358</td>
<td>1 x 1 GHz</td>
<td>32 bit</td>
<td>SGX530</td>
</tr>
<tr>
<td>Xilinx Zynq-7010</td>
<td>2 x 667 MHz &amp; 28k logic cell FPGA</td>
<td>32 bit</td>
<td>in FPGA</td>
</tr>
</tbody>
</table>

impressive despite only including power used directly by the servers, and not the reduction in associated costs due to less demanding cooling requirements. Given the enhanced processor specification of more recent Pi models, the power consumption improvements observed by Varghese et al. [15] may be even more dramatic on updated hardware. In order to take advantages of these savings, the SBCs have to be located in data centers with reliable connectivity—a service provided by at least two commercial hosting companies [16, 17].

Recent developments in SBCs have led to the introduction of more powerful peripherals being included on the SBC, one of these is demonstrated in the Up Squared board, which as well as having a Pentium processor has an on-board FPGA [18]. This functionality currently comes at the cost of a higher purchase cost, however, given time these peripherals might trickle down the market into the lower cost boards. Having an FPGA attached to each node in the cluster opens up new possibilities for compute paradigms. One challenge currently faced by SBCs including the Raspberry Pi are storage limitations because the lack of a high speed interconnect prevents fast access to large storage volumes, this limitation has been removed in other SBCs by the provision of SATA ports enabling standard HDDs/SSDs to be directly connected (see Table 3 for examples). As the capabilities of SBCs increase so too does the functionality of the clusters created from them.

### 3. Single board cluster implementations

A cluster can be defined as “a group of interconnected, whole computers working together as a unified computing resource that can create the illusion of being one machine” [19]. Previously clusters with large numbers of nodes were limited to large organisations able to afford the high purchase and running costs. Iridis 4, a cluster based at the University of Southampton cost £3.2M [20]. The same year the IridisPi cluster was built for a total cost comparable to that of a workstation [3]. Despite this being an extreme comparison, as when launched Iridis 4 was the most powerful supercomputer in a University in England, it puts the cost comparison in perspective. A more practical cluster would be made out of a few workstation / server nodes, however, this is still significantly more than the cost of a Pi cluster. This massive reduction in the cost of creating a cluster has enabled many companies and individuals who otherwise would not be able to afford a cluster to experiment, thus making cluster technologies more available to the hobbyist market. Along with companies these hobbyists have created a variety of different SBC clusters. Table 4 shows details of published SBC clusters. It is noticeable in this table that every cluster uses Raspberry Pi SBCs as the hardware platform. This is not to say that there are no clusters created using other platforms. Clusters have been created using the NanoPC-T3 [23], Orange Pi [24], Pine A64+ [25] and the Beagle Board [26].
Table 3: Example Single Board Computer platforms. All prices as of April 2018, TF cards are fully compatible with micro SD cards

<table>
<thead>
<tr>
<th>Board</th>
<th>SoC</th>
<th>RAM</th>
<th>Price</th>
<th>I/O</th>
</tr>
</thead>
<tbody>
<tr>
<td>Raspberry Pi 1 B+</td>
<td>BCM2835</td>
<td>512MB</td>
<td>$30</td>
<td>Audio, composite video, CSI, DSI, Ethernet, GPIO, HDMI, I2C, I2S, MicroSD, SPI, USB2</td>
</tr>
<tr>
<td>Raspberry Pi 2 B</td>
<td>BCM2836</td>
<td>1GB</td>
<td>$40</td>
<td>Audio, composite video, CSI, DSI, Ethernet, GPIO, HDMI, I2C, I2S, MicroSD, SPI, USB2</td>
</tr>
<tr>
<td>Raspberry Pi 3 B</td>
<td>BCM2837</td>
<td>1GB</td>
<td>$35</td>
<td>Audio, Bluetooth, composite video, CSI, DSI, Ethernet, GPIO, HDMI, I2C, I2S, MicroSD, SPI, USB2, WiFi</td>
</tr>
<tr>
<td>Raspberry Pi 3B+</td>
<td>BCM2837B0</td>
<td>1GB</td>
<td>$35</td>
<td>Audio, Bluetooth, composite video, CSI, DSI, Gigabit Ethernet, HDMI, I2C, I2C, MicroSD, PoE Header, SPI, USB2, WiFi</td>
</tr>
<tr>
<td>Raspberry Pi Zero W</td>
<td>BCM2835</td>
<td>512MB</td>
<td>$10</td>
<td>Bluetooth, composite video, CSI, GPIO, HDMI, I2C, I2S, MicroSD, SPI, USB2, WiFi</td>
</tr>
<tr>
<td>Odroid C2</td>
<td>S905</td>
<td>2GB</td>
<td>$46</td>
<td>ADC, eMMC/MicroSD, Gigabit Ethernet, GPIO, HDMI, I2S, IR, UART, USB</td>
</tr>
<tr>
<td>Odroid XU4</td>
<td>5422</td>
<td>2GB</td>
<td>$59</td>
<td>ADC, eMMC/MicroSD, Gigabit Ethernet, GPIO, HDMI, I2C, I2S, SPI, UART, USB, USB3.0</td>
</tr>
<tr>
<td>Pine A64</td>
<td>R18</td>
<td>≤2GB</td>
<td>$32</td>
<td>CSI, DSI, Ethernet, Euler, EXP, GPIO, MicroSD, RTC, TP, USB</td>
</tr>
<tr>
<td>OrangePi Plus 2</td>
<td>H7</td>
<td>2GB</td>
<td>$49</td>
<td>Audio, CSI, eMMC, Gigabit Ethernet, GPIO, HDMI, I2C, IR, SATA 2.0, SPI, TF, USB, WiFi</td>
</tr>
<tr>
<td>BeagleBone Black</td>
<td>AM335</td>
<td>512MB</td>
<td>$55</td>
<td>ADC, CANbus, Ethernet, GPIO, HDMI, I2C, eMMC/MicroSD, SPI, UART</td>
</tr>
<tr>
<td>UP Squared</td>
<td>N4200 &amp;</td>
<td>≤8GB</td>
<td>$289</td>
<td>ADC, Gigabit Ethernet (x2), GPIO, HDMI, mini-PCIe/m-SATA, MIPI (x2), RTC, UART, USB2, USB3</td>
</tr>
<tr>
<td></td>
<td>MAX 10</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Xilinx Z-turn</td>
<td>Zynq-7010</td>
<td>1GB</td>
<td>$119</td>
<td>CANbus, Gigabit Ethernet, HDMI, TF Card, USB2-OTG, USB, UART</td>
</tr>
</tbody>
</table>

Figure 2: A selection of SBC cluster cases.
(a) Iridis Pi [3], (b) Mythic Beasts [21], (c) Pi Cloud[4], (d) Prototype FRµIT Cluster of 6 nodes built using the Pi-Stack interconnecting board[22]
Despite these clusters being interesting prototypes none of them have been scaled beyond 10 nodes, for reasons for this are not clear.

One of the challenges when creating an SBC cluster is the physical arrangement, powering and communications required within the network. The clusters described in Table 4 are using totally bespoke hardware solutions which vary from Lego [3], through to wooden panels [27] or laser-cut acrylic [28], see Figure 2. These arrangements solve the problem of mounting and holding the SBCs but the challenge of power and network to the devices is not addressed. Arguably the neatest solution is that used by Mythic Beasts, which is to use Power Over Ethernet (PoE) to power the nodes [17]. This reduces the amount of cabling required. This neatness however, comes at a cost; each Pi needs a breakout board, and requires more expensive network switches. Fully managed PoE switches cost considerably more than unmanaged equivalents, but allow each Pi to be remotely power cycled.

Other solutions have used separate infrastructure for power and Ethernet. The approach first used in IridisPi was to use a separate power supply for each Pi. This has the advantage of a PSU failure only affecting a single board, however, it is not a compact solution. An alternative used by some of the clusters such as the beast [22] is multiple-output DC power supplies which reduce the space requirements. The third approach used by Beast 2 is to distribute DC around the system, and to have local power supplies for each Pi [28]. This reduces the required complexity of the power supply for each Pi, and by distributing a higher voltage (12V) the currents required are reduced, lowering the cable losses. There are two main approaches to networking, using a few large switches centrally within the cluster, and using multiple small (circa 8 port) switches distributed around the cluster. In both these cases the cable management can be a significant challenge.

The Federated Raspberry Pi μ-Infrastructure Testbed (FRPiIT) project has developed an interconnect board to address some of these requirements when building a Raspberry Pi or Pi compatible SBC cluster. The Pi-Stack [22] interconnect, shown in Figure 2(d) adds independent hardware power management and monitoring to each SBC node in the stack and provides a dedicated RS-485 channel for infrastructure management-level communication. Each Pi-Stack is allocated an unique address on the RS-485 bus and a total of 16 Pi-Stack interconnect boards can be combined into a single cluster stack, each supporting two Raspberry Pi compatible SBCs. This limits each cluster stack to 32 nodes, providing a suitable power supply is available. This limit can be overcome by combining multiple stacks to form a single cluster. The RS-485 management interface provides instantaneous current and voltage monitoring for each node as well a customisable heartbeat to ensure the node OS is functioning. Nodes can be notified of an impending reset or power off through an interrupt, this ensures that nodes are provided with sufficient notice to cleanly shutdown. Each node can be power cycled independently, so powering up the cluster can be staged to avoid peak current power supply load issues. This also facilitates disabling unwanted compute and/or improves thermal management to avoid overheating issues. All SBC nodes in a stack are supported via four conductive threaded stand-offs. Two of these supply power to the Pi-Stack interconnect, and hence to the compute nodes; the other two are used for the RS-485 management-level communication. Using the stack chasis for power and communication drastically reduces the cabling required; although the Pi-Stack has headers and isolation jumpers to support cabling if required. Since the Pi-Stack interconnect has on-board power regulators we can power the stack, through the chassis using a wide range of voltages (12V-24V). This is important to reduce the current as the number of nodes in the stack gets larger and also makes connections to batteries or Photovoltaics (PVs) simple.

Commercial products to simplify the creation of Raspberry Pi clusters are now being produced. One example is the ClusterHAT [29] which enables 4 Pi Zeros (or Zero W) to be mounted on top of a Pi 2/3 which then provides network (via USB gadget) and power. The central Pi acts as a coordinator managing all traffic, and can power down the Pi Zeros when they are not needed. Another product is the BitScope Blade [30]. The Blade allows either 1, 2 or 4 Pis to be mounted on a stack-able back plane and can be powered with 9 - 48 volts, and the provides local 5 volt power supplies, this significantly reduces the current needed through the back plane. This product was used in the creation of the cluster at Los Alamos [31]. As well as the development of products designed to make the creation of bespoke clusters easier it is possible to buy entire SBC clusters of up to 20 nodes off the shelf (not limited to just Pi based clusters) [32].

SBCs have facilitated the creation of multiple clusters at a variety of scales but have some disadvantages compared to a traditional cluster, these include:

- limited computing resources (CPU, memory, storage) per node.
- increased hardware failure rate
- high network latency
- the need for architecture specific compilers and tooling
- potentially mixed hardware (where different SBC are used)

The first two of these disadvantages can be partially mitigated by expanding the cluster, a proposition made possible by the low-cost and power consumption of the nodes. By increasing the size of the cluster the effect of a single node failing is reduced, and the low-cost means replacement is easy. The high network latency is harder to mitigate against. This high network latency is particular
prevalent on the earlier boards in the Raspberry Pi series of SBCs which use a USB 2 100MBps network adapter rather than a direct connection to the processor. The Raspberry Pi 3B+ addresses this by using a gigabit adapter, but it is still limited by the USB2 connection. Other boards have chosen a USB 3 gigabit network adapter [33] which should reduce the network latency for the cluster.

The design and creation of the cluster is only the first step in using a SBC cluster. In addition to the relatively high failure rate of SBC hardware every cluster needs ongoing maintenance, both to ensure security vulnerabilities are patched in a timely manner, and to make sure that new packages are available. The differences in per node storage, RAM and processing power in SBC clusters when compared to traditional high performance compute nodes means that different tools and techniques are needed to manage this.

4. Use cases

This section outlines the broad domains in which SBC clusters might be deployed. In some cases, researchers have only identified a potential use case, in order to motivate the construction of SBC clusters. In other cases, researchers report prototype deployments and initial evaluation results.

We classify use cases into various categories. This is not intended to be an exhaustive list for characterizing future use cases; it is simply a convenient means of grouping use cases we have identified from the current literature.

4.1. Education

The construction of the earliest Raspberry Pi clusters, for instance at Southampton [3], Glasgow [4] and Bolzano [35], was to provide a hands-on educational experience. Educational exposure to real clusters is difficult and often limited to using a handful of PC’s or simulated environments. These SBC clusters are low-cost, quick to build and offer challenges around the physical construction. They are typically under 100 nodes, are connected by Ethernet, powered via USB hubs and run a custom Linux-based software stack.

These SBC clusters or micro-scale data centers [4] can provide many relevant experiences for students, in terms of hardware units (routers, racks, blades) and software frameworks (such as Linux, containers, MPI, Docker, Kubernetes) which are typical components in state-of-the-art data centers. Not all data center aspects are replicated in the SBC clusters, for example network bandwidth, computational power, memory and storage capacities are considerably lower, limiting the cluster to small-scale jobs. Node power management and cluster network topologies are not replicated; although some work has investigated network topologies [35].

Such micro data centers have been used at various universities for undergraduate classes and projects. To date, the emphasis appears to be on the experience gained in building the cluster rather than subsequent operation [39, 40], although the SeeMore cluster [6] is primarily intended to be a modern art installation. To the best of our knowledge, there have not yet been any systematic studies of the pedagogical benefits of hands-on experience with micro clusters.

The Edinburgh Parallel Computing Centre (EPCC) built Wee Archie [58], an 18-nodes Raspberry Pi 2 cluster, aiming to demonstrate applications and concepts relating to parallel systems. Several Beowolf clusters have targeted education, using a variety of SBC configurations [41]; 2-nodes ODROID, 6-nodes NVIDIA Jetson TK1, and 5-mixed-nodes of 1 NVIDIA Jetson TK1 and 4 Raspberry Pis. Introducing High Performance computing (HPC) and using the Message Passing Interface (MPI) library to develop the applications is addressed by several clusters [12, 3]. It is not just academic institutions creating Raspberry Pi clusters for educational purposes; GCHQ built a cluster of 66 nodes in as a teaching tool for their internal software engineering community [36].

4.2. Edge Compute

Many sensor network architectures form a star topology, with sensors at the edge and storage and compute power in the middle, for example cloud-based solutions. The advantages of this are that the data is all stored and processed in one location making management and post-processing of the data less complex. The disadvantage is that all the data has to be transmitted from the data producers to the centralised storage and compute resources. On large scale deployments this means transmitting across bandwidth constrained wireless networks or expensive satellite uplinks. There is a need to be more efficient with data bandwidth, transmitting only the data that is required. Furthermore by processing the data at its origin we can reduce the bandwidth requirements by only transmitting processed data; for example threshold alerts or cumulative data. This requires computational power to be connected to the data producers and is referred to as Edge Compute [43, 44], in cases where the compute is moved closer to the data producers but is not attached it is referred to as Fog Compute.

The system becomes more decentralised as the intelligence is pushed out towards the edge, improving latency as the system can react to local events [45]. Latency reduction is important for virtual/augmented reality and gaming applications. It is a key priority for 5G networks. As well as improving network utilisation by reducing traffic, Edge Compute can potentially improve the system reliability, for example by enabling data producers to operate through connectivity outages. Further, minimizing data transfer can improve user privacy [40] for example by keeping personal or identifiable information local, transmitting only anonymised data.

The overhead of setting up a cluster is greater than that of using a single machine and more hardware increases the
Table 4: Example Raspberry Pi Clusters

<table>
<thead>
<tr>
<th>Name</th>
<th>Year</th>
<th>Owner</th>
<th>Hardware</th>
<th>Use cases</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pi Co-location [16, 34]</td>
<td>2013</td>
<td>PC Extreme (NL)</td>
<td>2500 Pi1</td>
<td>Pi hosting provision</td>
</tr>
<tr>
<td>National Laboratory R&amp;D Cluster [31]</td>
<td>2017</td>
<td>Los Alamos National Lab (USA)</td>
<td>750 Pi3B</td>
<td>R &amp; D prior to running on main cluster</td>
</tr>
<tr>
<td>Bolzano Cloud Cluster [35]</td>
<td>2013</td>
<td>Free University of Bolzano (ITA)</td>
<td>300 Pi1</td>
<td>Education, research, &amp; deployment in developing countries</td>
</tr>
<tr>
<td>The Beast [27]</td>
<td>2014</td>
<td>Resin.io (UK)</td>
<td>120 Pi1</td>
<td>Demonstrating/testing distributed applications</td>
</tr>
<tr>
<td>The Beast 2.0 [28]</td>
<td>2017</td>
<td>Resin.io (UK)</td>
<td>144 Pi2</td>
<td>Demonstrating/testing distributed applications</td>
</tr>
<tr>
<td>Pi Hosting [17, 21]</td>
<td>2016</td>
<td>Mythic Beasts (UK)</td>
<td>108 Pi3B / 4U rack</td>
<td>Pi hosting provision</td>
</tr>
<tr>
<td>Raspberry Pi Cloud [4]</td>
<td>2013</td>
<td>University of Glasgow (UK)</td>
<td>56 Pi1 &amp; 14 Pi2</td>
<td>Research and Teaching light-weight virtualisation</td>
</tr>
<tr>
<td>Bramble [36]</td>
<td>2015</td>
<td>GCHQ (UK)</td>
<td>66 Pi1</td>
<td>Internal teaching tool</td>
</tr>
<tr>
<td>Iridis-Pi [3]</td>
<td>2013</td>
<td>University of Southampton (UK)</td>
<td>64 Pi1</td>
<td>Education</td>
</tr>
<tr>
<td>Wee Archie Green [37, 38]</td>
<td>2015</td>
<td>University of Edinburgh (UK)</td>
<td>19 Pi2</td>
<td>Education Outreach</td>
</tr>
</tbody>
</table>

There are two key reasons to consider SBC clusters for edge compute, rather than purchasing a single machine or traditional cluster. i) The SBC cluster can be sized closer to the workload demands, keeping utilisation high, reducing costs and potentially keeping power requirements lower. The cost increment per node to expand the cluster is also much lower than adding nodes to a traditional cluster. This is particularly of benefit where a large number of edge compute clusters are deployed. ii) Clusters can be configured to be more resilient to hardware failures offering compute and storage redundancy.

One of the main drivers for Edge Compute is the increasing popularity of Cyber Physical Systems (CPS) or Internet of Things (IoT) with some predictions stating that there will be 30 billion IoT devices by 2020 [47]. This may be an over estimate but demonstrates a huge demand and opportunity for embedded systems and hence SBC based devices. There is a need to decentralise the storage, compute and intelligent of systems if they are to scale to such large volumes.

4.3. Expendable Compute

We believe that low-cost SBCs introduces the concept of single-use disposable or expendable compute. This has the potential to extend compute resources into hostile, high risk environments but will require responsible deployment to avoid excessive pollution. For example ad-hoc networks and systems in the wild on volcanoes [48] and in rainforest [49].

We propose that SBC clusters might also be considered as expendable compute resources, providing new opportunities. These SBC clusters can provide significant computational power at the edges of IoT deployments, for example to drastically reduce bandwidth requirements [7] and promote autonomous, fault tolerant systems by pushing compute and hence logic out towards the edge. These expendable clusters are a new class of compute to facilitate IoT architectures.

SBC clusters require additional infrastructure to operate, for example in traditional data centers as the cluster size increases the cost of this network and power infrastructure dwarfs the compute costs [50].

4.4. Resource Constrained Compute

SBC clusters provide a new class of computational resource, distinct from others by their low-cost. This provides an opportunity to place compute clusters in new and imaginative places, in particular where physical size and power consumption is restricted, for example backpack contained clusters, or solar powered deployments.

Many SBC clusters are low-power ARM based SoC which lend themselves to high core densities and are generally considered low-power. Since many Edge Compute architectures require remote deployments powered via renewable energy there is a need to measured the trade-offs between performance and energy-consumption; often measured in MFLOPS/watt. For example, currently the top of green supercomputer [51] is DGX SATURNV, owned by...
NVIDIA, which provides 9462.1 MFLOPS/watt. There have been numerous studies which benchmark ARM power consumption.

- A benchmark of 10 types of single board computer using Linpack and STREAM showed the Raspberry Pi 1 Model B and B+ produced 53.2 and 86.4 MFLOPS/watt respectively.

- A benchmark of 3 different configurations of 8-nodes clusters using Linpack showed the Raspberry Pi 1, Banana Pi, and Raspberry Pi 2 clusters provided 54.68, 106.15, and 284.04 MFLOPS/watt respectively.

- A 4-node cluster using Parallela boards, each with a single ARM-A9 CPU and a single 16-core Epiphany Coprocessor was used to develop a floating-point multiplication application to measure the performance and energy consumption. Further power measurements or MFLOPS/watt results were not presented.

- A comparative study of data analytics algorithms on an 8-node Raspberry Pi 2 cluster and an Intel Xeon Phi cluster achieves better energy efficiency for the Apriori kernel whereas the opposite is true (Xeon Phi is more energy efficient) for K-Means clustering. For both algorithms, the Xeon Phi gives better absolute performance metrics.

We can see from this that the MFLOPS/watt still needs improvement and most of these benchmarks do not include the energy required to cool their clusters as they are passively cooled; larger deployments will probably require active cooling.

As the CPUs on the SBCs become more advanced there is a trend for the MFLOPS/watt to increase. Better utilisation of power will extend the reach of the SBC clusters, making them better candidates for renewable energy or battery powered deployments. We predict that key areas where SBC clusters can make an impact, will rely on limited power sources making this an important attribute. The current SBCs go some way to efficient power usage and in the future we can expect this to improve; mainly due to the inclusion of ARM based CPUs and ability to be passively cooled.

4.5. Next-Generation Data Centers

Traditionally SBCs are 32-bit which is in contrast to most data centers which are 64-bit, although more recent SBCs are based on 64-bit SoCs as shown in table 2. Data centers have traditionally used Intel/AMD based servers, but more recently some ARM servers exist such as HP Moonshot servers. The Open Compute Community is also working on ARM based designs in conjunction with Microsoft and Cavium.

Projects such as Euroserver are leading the initiative for ARM clusters, the key motivation is to improve data center power efficiency. ARM cores may provide one order of magnitude less compute resource than Intel, however, it is possible to pack ARM cores much more densely, which means exascale sized platforms are potentially viable. We propose that ARM based SBC clusters make reasonable low-cost testbeds to explore the next generation of ARM based data centers.

The Mont Blanc project has investigated how the use of 64-bit ARM cores can be used in order to improve the processor efficiency in future HPC systems. The SpiNNaker project has identified that the simulation of the human brain using traditional super computers places extremely large power demands on the electrical infrastructure and so has developed SpiNNaker chips which require substantially less power per second of neuron simulated, by using multiple low power processors instead of fewer more powerful processors.

Next-generation data centers may offer physical infrastructure as a service, rather than using virtualization to multiplex cloud users onto shared nodes. This reduced the software stack and potentially improves performance by providing cloud users with physical access to bare metal hardware. Some cloud hosting providers already sell data center hosted, bare metal Raspberry Pi hardware as a service. By using SBC hardware we can explore the capabilities of next generation physical infrastructure as a service, either as standalone single nodes or as complete cluster configurations.

4.6. Portable Clusters

Portable clusters are not new, they tend to be ruggedised units ranging in size from a standard server rack to a whole shipping container. They are often deployed into environments with either mains or generator power supplies. We believe that SBC clusters give rise to truly portable clusters, for example in a backpack. The power requirements mean that portable clusters can be battery powered, and powered on-demand.

This is an advantageous for example by first respondents to large scale disaster recovery as they can be carried by drones, aircraft, ground vehicles and personnel. It also gives rise to more mobile and dynamic Edge Compute topologies that do not rely on fixed location sensors. Some of the advantages of edge compute in emergency response scenarios are discussed in.

5. Single Board Cluster Management

The purpose of cluster management is to transform a set of discrete computing resources into a holistic functional system according to particular requirement specifications, and then maintain its conformance for any specification or environment change. These resources include, but not limited to, machines, operating system, networking, and workloads. There are at least four factors...
that complicate the problem: dependencies between resources, specification changes, scale, and failures.

Whereas Section 3 reviewed the hardware underlying SBC clusters and Section 4 outlined the range of applications running on SBC clusters, this section concentrates on the software infrastructure required to manage SBC clusters. Figure 3 shows a schematic diagram of a cluster, with associated cross-cutting management tasks on the left-hand side. The rest of this section will consider typical software deployed at each level of the infrastructure stack, along with management activity.

5.1. Per-Node OS

Most SBCs are capable of running a range of mainstream OS variants. Some simply support single-user, single-node instances, such as RISC OS. Others are IoT focused but do not have traction in the open source community, such as Riot-OS [64], Contiki OS [70], and Windows 10 IoT Core [71]. The most popular SBC OS is Linux, however it usually requires specific kernel patches for hardware device drivers which are only available in vendor repositories.

End-user targeted Linux distributions such as Raspbian [72] or Ubuntu [73] are bloated with unnecessary software in tens of thousands of packages. These OS installations are fragile to security attacks as well as increasing the size and complexity of updates for a large cluster. In contrast, Alpine Linux [74] is a lightweight distribution. The disk image requires around 100MB rather than the multiple GBs of Raspbian. Alpine has a read-only root partition with dynamic overlays, which mitigates problems with SD card corruption. It supports security features like Position Independent Executables to prevent certain classes of exploits.

Customized OS generators such as Buildroot [75] and OpenEmbedded/Yocto projects [76, 77] provide automated frameworks to build complete, customised embedded Linux images targeting multiple hardware architectures. They use different mechanisms to accomplish this [78]. Buildroot has a simple build mechanism using interactive menus, scripts and existing tools such as kconfig and make. Although it is simple to build a small image, it is necessary to rebuild a full image in order to update the OS since there is no package management. OpenEmbedded/Yocto addresses this problem by applying partial updates on an existing file system using a layer mechanism based on loboSTree [79]. This allows low bandwidth over-the-air updates [80, 81] with shorter deployment times and support for rollback. The main drawback of these OS generators is their increased configuration complexity.

Another alternative is LinuxKit [82], a toolkit for building a lean OS that only provides core functionality, with other services deployed via containers. The project is still immature (just released in April 2017) and only supports x86 platforms.

In terms of OS image deployment on each node, early systems worked by manually flashing a selected OS image on persistent memory such as SD card. Cox et al. [8] note that this is time consuming and unsuitable for large scale clusters. Abrahamsson et al. [83] copy a standard OS image onto each SD card. The image includes boot scripts that automatically requests configuration files from a known master node, deploys these files to initialize per-node resources, and performs the registration. Once a node has joined the cluster, then the manager can control the node remotely to run workloads or unregister the node.

A more recent, superior approach relies on network boot [84, 85], allowing each node to be diskless. This simplifies OS updates and reduces the number of resources to be maintained. Many SBC nodes do not support network boot, or only limited protocols at best. iPXE [85] is a likely candidate for network boot, and supports ARM platforms.

5.2. Virtualization Layer

Virtualization enables multi-tenancy, resource isolation and hardware abstraction. These features are attractive for large scale data centers and utility computing providers. Micro data centers can also benefit from these virtualization techniques.

Hypervisor based virtualization systems such as Microsoft Hyper-V [86], Xen [87], and VMware [88] tend to be resource heavy and are currently unsuitable for resource limited SBCs. Linux containers facilitate a more lightweight virtualization approach. Tso et al. [4] run OS-level virtualized workloads based on Linux’s cgroups functionality. This has much lower overhead than full virtualization, enabling a Raspberry Pi node to run several containerized workloads at the same time. Popular lightweight virtualization frameworks include Docker [89] and Singularity [90]. These are suitable for deployment on resource constrained SBCs. Morabito [91] reports a comprehensive performance evaluation of containerized virtualization, and concludes this abstraction has minimal performance overhead relative to a bare-metal environment.

Singularity provides a mobility of computer solution by wrapping operating system files into a container image, so that the application runs as the user who invoked it, preventing it from obtaining root access from within a container. Singularity can run on kernels before Linux kernel version 3.10 without any modification (unlike Docker).

Some SBC clusters take advantage of unikernels, for example MirageOS [92] to enable a secure, minimal application footprint on low power devices. Unikernels can run on hypervisors or directly on hardware and only support the OS features required by the application, hence drastically reducing the host OS footprint.

There are many useful management tools for container based workloads that assist with the deployment and lifecycle of containers, for example Mesos [93], Docker Swarm [94], Kubernetes [95, 96].
5.3. Parallel Framework

A parallel framework presents a cluster of nodes as a unified, logical entity for user-level applications. This is appropriate for scientific, big data, and utility computing domains. Such frameworks require each node to run a local daemon or instance of a runtime system.

Many SBC clusters, e.g. [97, 38, 3], are designed to run HPC workloads based on MPI. In order to improve performance, software libraries might need to be recompiled since distributed packages are generally not specialized for SBC nodes. Due to per-node memory restrictions, the MPI jobs are generally small scale example applications rather than calculations of scientific interest.

Some clusters run virtualized workloads, to provide utility computing services. The Bolzano cluster [35] hosts OpenStack, although the authors report poor performance because this complex framework requires more resources than SBCs can provide.

Schot [98] uses eight Raspberry Pi 2 boards to form a miniature Hadoop cluster, with one master and seven slave nodes. He uses standard tools including YARN for workload management and HDFS for distributed storage. This cluster uses DietPi, a slimmed down Debian OS variant. Test results show that the cluster can utilise more than 90% of the 100 Mbps Ethernet bandwidth when transferring data from node to node. This is a single tenancy cluster, effectively a platform for performance tests. There is no capability for failure recovery, since a failing master node is not automatically replaced. Other SBC clusters run Hadoop or Spark workloads [3, 99, 100] with similar configurations. Major frustrations appear to be insufficient per-node RAM, high latency network links, frequent SD card corruption leading to HDFS failure, and poor performance of the underlying interpretive Java virtual machine.

Many of these difficulties are intrinsic to SBC clusters, suggesting that industry standard parallel frameworks will require significant re-engineering effort to make them effective on this novel target architecture.

5.4. Management

A cluster management system reduces repetitive tasks and improves scalability. Management tasks cut across the cluster stack, as shown in Figure 3.

A key problem is node reachability, when SBCs are deployed behind NATs or firewalls. Existing configuration frameworks assume always-connected nodes in a local area network scenario.

An update system is critical to any cluster because all nodes must be updated regularly. A traditional approach involves updating the root partition and then rebooting the machine when it is needed. However, an error (e.g. I/O error) may occur during update which can make the machine unable to boot properly afterwards due to a corrupted root partition. Several frameworks [106, 107] address this problem by having two root partitions, one has the current root filesystem and the other has a newer version. Whenever an update error occurs, the machine can easily rollback by booting from an older version partition. This also allows Over-The-Air (OTA) updates since the target partition is not being used by the running system.

Relying on a single server to provide the updates is not an ideal solution, in particular for a large cluster, because all nodes are likely to download updates at the same time, potentially overloading the server with an overwhelming demand for bandwidth. One alternative approach [108] assigns a randomized delay for each node when it starts the download. By adjusting the randomization interval, we can adapt the update mechanism to the scale of the
system. However, this does not solve a single point of failure problem. Another approach would be to balance download requests across multiple mirror servers, which has been the main solution of most Linux distributions. An alternative might be employing a peer-to-peer update mechanism to optimize network bandwidth efficiency by allowing a client to download from other clients [109].

Other cluster management facilities are important, including monitoring and analytics, resource scheduling, service discovery, and identity and access management. These are all standard cluster services, which must be configured appropriately for individual use cases. As far as we are aware, there are no special requirements for SBC clusters.

6. The Future of the SBC Cluster

We see a strong future for SBC clusters in two primary areas. First, and most importantly, SBC clusters are an ideal platform for Edge and Fog computing, cyber-physical systems, and the Internet of things. These all require customisable, low-power, and ubiquitous deployment of compute nodes with the ability to interact with the environment. SBC clusters are well suited to this environment, with the Raspberry Pi being an exemplar of the type of devices considered, but we expect to see an increasing transition to SBC platforms that are engineered for robust long-term deployments, rather than hobbyist projects. For instance, certain types of neural networks [110, 111] are being adapted to run effectively on SBC clusters, with low power constraints, limited memory usage, and minimal GPU processing.

Secondly, we expect continued use of SBC clusters to support educational activities and low-end hosting services. To maintain relevance, educators must teach cluster computing – the era of stand-alone machines is at an end – although building full-fledged data centers is beyond the means of most academic institutions. SBC clusters still have a powerful role to play in educating the next generation, as scale model data centers, but increasingly to teach Edge computing, CPS, and the IoT. Following from this, we expect to see a rise in low-end hosting services that allow hosting on a dedicated SBC rather than a virtual machine, catering to those educated on SBCs such as the Raspberry Pi.

The consequences of these developments will be increasing heterogeneity in terms of devices, deployment environments, and applications of SBC clusters, coupled with use in increasingly critical applications and infrastructure. This has implications for SBC hardware development and for the supporting software infrastructure.

On the hardware side, we expect to see divergence in new SBC designs with some becoming specialized for cluster deployments, with a focus on compute and network performance, while others support increasingly heterogeneous I/O interfaces and peripheral devices. SBC designs will become more robust and gain remote management features, for example, higher-quality flash storage, network boot, and PoE.

We expect the range of hardware configurations to increase as new vendors enter the market, and as devices are increasingly customised to particular applications. The implication of this is that there will likely be no standard device configuration that can be assumed by SBC operating systems and management tools: rather there will be a common platform core, that’s device independent, with a range of plug-ins and devices drivers for the different platforms. A key challenge will be providing stable and flexible device APIs so the core platform can evolve without breaking custom and/or unusual hardware devices and peripherals. Systems such as Linux provide a stable user-space API, but do not have a good track record of providing a stable in-kernel device driver API.

In terms of the software and management infrastructure, the heterogeneity of SBC cluster hardware, deployment environments, and applications forces us to consider issues that do not occur in traditional data center networks. Specifically, SBC cluster deployments may have no standard device hardware configuration; will run on devices that tend to be relatively underpowered, and may not be able to run heavy-weight management tools due to performance or power constraints; and will run on devices that have only limited network access due to the presence of firewalls, Network Address Translation (NAT) or intermittent connectivity and that cannot be assumed to be directly accessible by a management system.

The issue of limited network connectivity is a significant challenge for the management infrastructure. Traditional data centers are built around the assumption that hosts being managed are either directly accessible to the management system, or they can directly access the management system, and that the network is generally reliable. This will increasingly not be the case for many SBC deployments. There are several reasons for this:

1. devices will be installed in independently operated residential or commercial networks at the edge of the Internet, and hence will be subject to the security policies of those networks and be protected by the firewalls enforcing those policies.
2. since they’re in independently operated network devices may be in different addressing realms to the management system, and traffic may need to pass through a NAT between the device being managed and the management system.
3. devices may not always be connected to the Internet, perhaps because they are mobile, power constrained, or otherwise have limited access.

Traditional cluster management tools fail in these environments since they do not account for NAT devices and partial connectivity, and frequently do not consider intermittent connectivity. Tools need to evolve to allow node management via indirect, peer-to-peer, connections that traverse firewalls that prohibit direct connection to the
system being managed. They must also incorporate automatic NAT traversal, building on protocols such as STUN and ICE [112, 113] to allow NAT hole-punching and node access without manual NAT configuration (as needed by peer-to-peer management tools such as APT-P2P [114] and HashiCorp Serf today [115]). Existing cluster management tools scale by assuming a restricted, largely homogeneous, network environment. This is not the typical deployment environment for SBC clusters. They will increasingly be deployed in the wild, at the edge of the network, where the network performance and configuration is not predictable. Management tools must become smarter about maintaining connectivity in the face of these difficulties to manage nodes to which there is no direct access.

Finally, there are significant social and legal implications to the use of massively distributed SBC cluster platforms. As noted above, developing management tools to work within the constraints of different security and addressing policies is one aspect of this, but there are also legal implications of managing a device that may be in a different regulatory environment, on behalf of a user subject to different laws. The implications for trust, privacy, security, liability, and data protection are outside the scope of this paper, but are non-trivial. They will become increasingly critical as personal data migrates into Edge compute platforms based on SBC clusters, and as those platforms control increasingly critical infrastructure.

7. Conclusion

In this paper we have shown that SBCs are a computational game changer, providing a new class of low-cost computers. Due in part to their low-cost and size they are utilised for a wide variety of applications. This popularity has ensured that newer and more advanced SBCs are constantly being released. We have shown that often these SBCs are used to build clusters, mainly for educational purposes. These clusters are well suited to educational applications due to their low-cost, but they also make good testbeds for newer data center technologies, for example high core-density and ARM based architectures. In IoT architectures there is a move away from a centralised compute resource, to an architecture where the computational power is pushed out closer to the edge, for example near data generating sensors. SBC clusters are an ideal candidate for Edge Compute because of their power requirements and size. It is also possible to power off some or all of a SBC cluster, powering on only when computational resources are required, thus saving power and coping with burst computational demands, for example audio, video or seismic data processing based on triggers. We identify that the maturity and advancing features in SBCs will translate into more powerful, optimised and feature rich SBC based clusters.

We have identified multiple use cases for SBC clusters and see a strong future, especially as more advanced SBCs become available. Current cluster management technology has limitations for Edge Compute and the physical cluster construction is currently rather bespoke. Through the FRµIT project we are currently working on tools and techniques to simplify the software stack, support containerisation and facilitate the update and maintenance of large numbers of distributed Edge Compute clusters. The FRµIT project also manages the physical construction of Raspberry Pi compatible clusters, through the introduction of a Pi-Stack interconnect board which includes both power and OS management capability, as shown in Figure 2(d) this reduces cabling, focuses air flow and adds independent power monitoring & isolation.

Small, portable, low-power clusters are the key to improving IoT architectures and overcoming limited or costly bandwidth restriction. As processor performance improves and SBC hardware advances, so will the clusters upon which they are based, unlocking new capabilities. If small, low cost, portable clusters are to become mainstream, we acknowledge that there is an overhead to convert applications to utilise clusters, rather than as single multi-core processor. We believe that as CPU speeds have plateaued and performance is achieved by increasing the number of cores, more applications will support multi-core and increasingly support clusters. SBC based clusters are a new and distinct class of computational power. Although in their infancy have the potential to revolutionise the next generation of sensor networks, and act as a fantastic exploratory tool for investigating the next generation of data centers.

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References

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